

An ST7 based microcontroller 6 channel lighting sequencer by Tony Ashton



Introduction

This article is concerned with looking at a method of controlling 6 x 100 watt disco lights operating at 240Volts AC. The project will use minimal hardware and allow the user to update the sequence of the lights within the club environment. These disco lights are controlled by the Kanda ST7 Starter Kit, or flash circuit, which is shown in Appendix 2 later on in this article, together with the use of another circuit that will allow the operation of the mains supply voltage. The Kanda ST7 Starter Kit was designed to provide maximum flexibility to users of the ST7 family of high-performance microcontrollers. The Kanda Starter Kit offers many features such as using the 16-bit timers to produce Pulse Width Modulation (PWM) control. The project was designed around the ST72254G2 flash Integrated Circuit (IC) which is a 32-pin flash device and will allow the code to be updated easily when in operation. The Kanda ST7 Starter Kit, with a flash device, or an alternative circuit based around the ST72254G2 device

will be connected to a power controller circuit in the way shown in this article, in order to provide control of the disco lights.

Figure 1 shows how the Kanda ST7 Starter Kit or an equivalent circuit that incorporates the ST72254G2 flash device is connected to the controller board. A lead made up of seven wires will be taken from the Kanda ST7 Starter Kit to the power controller board. Six of these wires will be connected



from PB2-PB7 and the other to VDD, of the Kanda ST7 Starter Kit in order to provide 5 volts for the triac driver circuits . Each of these seven wires will be taken to a 9-way 'D' type connector on the power controller board, this is shown in Figure 2.

Power Controller Circuit

The power controller circuit uses triacs, and optocoupler triac drivers, the circuit diagram of the power controller board can be seen in Figure 2.

Figure 2 uses the Z0410DF triac, and the MOC3020 optocoupler triac driver which when combined together will provide smooth power control. A common problem within power switching circuits is that triacs switch from off state to the on state in a very fast time, and can produce extremely rapid increases in load current. Such a current step will produce harmonics within the circuit and can cause interference to AM radio reception. A simple way to prevent this would be to incorporate a capacitor and an inductor in the schematic to form a low pass filter and hence prevent the harmonic energy from reaching the load wiring and radiating everywhere. This filter will therefore reduce the interference to a nearby AM radio receiver. However the above circuit uses the MOC3020 optocoupler triac driver and can be used as a good noise barrier for digital signal isolation. The ground loop is also broken and any noise picked up by the cabling appears as a common mode noise at the terminals, and hence can be easily cancelled either solely by the optocoupler or using additional common mode chokes, thus eliminating the need for the low pass filter.

Protection Tips

The project has been designed to be used with $6 \ge 100$ watt light bulbs, at a working voltage of 240 volts however, great care must be taken when working with this type of voltage. When the circuit has been produced and built it is recommended that it is

> inserted into a hard plastic case, and that all wires entering and leaving the box should be securely fixed, and that no bare wires can be seen. After the circuit has been built and checked for visual defects, the circuit was tested using a continuity tester in order to detect any



short circuits that might have arisen. When the circuit is running, the box must be kept in a closed position and nothing must be inserted in to the box. If any problems arise make sure that the power is disconnected from the mains supply before investigations start. A 3.0 Ampere fuse must be fitted to the circuit when working with 6 x 100 light bulbs operating a 240 volts AC.

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User Instructions

Before the power controller circuit is built make sure that the dimensions of the PCB will fit securely into the plastic case, and that fixing screws can be inserted into the board without short circuiting the PCB. The 9-way 'Male' 'D' type socket can be fitted to the PCB ensuring that it is placed close to the side of the box. The side of the plastic case can then be cut out in order for the 'Male' 'D' type socket to be accessed. It should also be ensured that the six 2-way terminal blocks for the connection of the lights are towards the front of the plastic case. The plastic case can then be drilled out in order for the cable of the disco lights to sit easily in the terminal blocks. The same procedure should be carried out for the mains cable



which can be placed at the back of the circuit. If the cables are loose in the drilled holes then a grommet and cable clamps should be fitted for added protection. A seven way cable is then required with the 9way 'Female' 'D' type socket connected to one end, and seven single in-line sockets connected to the other, these seven will then go to PB2-PB7 (pins 4-7, and 10-11), and VDD (pin 32) of the Kanda ST7 Starter Kit respectively. A diagram showing the connections can be seen in Figure 3.

Once the board has been tested and all the connections are secure, power can be supplied to the board. The initial start off will set the sequences to 15%, which will be the fastest sequence of the lights. However the Kanda ST7 Starter Kit can control the speed settings of the sequence of lights by simply

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pressing keys 0 - 3, the settings are shown below:

Button '0'	Sets Sequence to '15%'
Button '1'	Sets Sequence to '25%'
Button '2'	Sets Sequence to '50%'
Button '3'	Sets Sequence to '75%'

If button 3 on the Kanda ST7 Starter Kit is pressed the sequence of lights will operate at 75% which is the longest time interval between on/off of the lights. So this means that for the whole period the lamp is on for three-quarters of the time and off for the other quarter. At any time the user can select another speed such as button 2 and the sequence of the lights will increase to 50% and decrease the on/off time of the lights.

Software Design

The software was designed using the ST7 Starter Kit Assembler code. The software listings generated can be seen in Appendix 1 of this article. It will be noticed that there are three stages to the software:

The .sts which is the Set-up code The .stv which is the Vector code The .stm which is the Main code

The Set-up file will generate all the code necessary to set the ports of the ST7

microcontroller as inputs or outputs. The set-up code is also used to set the timers, and enable the interrupts. Once the interrupts have been enabled they can be manipulated within the vector file. The reset vector information can also be found in the vector file. The main file will then be left so as the user can input the main body of the code. Using the three different files in the way explained above keeps the code clear and easy to follow. If changes have to be made to the code at a later date it will be easy for the user to find the correct place to insert the new code.

In order to generate the necessary time delays between the flashing lights, the programmable timer was set-up to be a 16bit free running increasing counter with its associated 16-bit registers. The two readonly 16-bit registers contain the same values but with the difference that reading the ACLR (Alternate Counter Low Register) does not clear the timer overflow flag. However writing in the CLR (Counter Low Register) or ACLR resets the free running counter to the FFFCh value. The timer clock depends on the clock control bits of the CR2 register. The value in the counter register repeats every 131.072, 262.144 or 524.288 internal processor clock cycles depending on the CC1 and CC0 bits. When the program is entered the counter is set to the default

value of 15 percent; this is achieved by the following code.

ld a,#\$26 ld TAOC1HR,a ld a,#\$66 ld TAOC1LR,a clr TAACLR

Set:

The code shown above is used to set the default value for the counter in the Timer A Output Compare 1 Register (TAOC1R). When the code is running the value 2666h appears in the TAOC1R. The last line of the above code is used to set Timer A Alternate Counter Register (TAACR) to FFFCh. This is an increasing counter which will proceed to 0000h on the next clock cycle after FFFFh. When the value in TAACR matches the value in TAOC1R (in this case both set to 2666h) an interrupt is generated. This starts the sequence of lights at the default speed and will continue at this setting until buttons 0-3 on the Kanda ST7 Starter Kit are pressed which will change the speed of the sequence of lights. The code was designed to allow the user to update the sequence of lights within the vector file, which can then easily be transferred to the flash device by using a keyfob. continued on page 32

Catum Cad		; ADD LIGHT SEQUENCE HERE [11]	
Set-up Cod	e	***************************************	
;ST7 Setup File	- Created by Application Builder	light_1: nop	
; Setup File Cre	ated with Device Set to ST72C254G2	bres pbdr, 12	
.Include \$Device	Name ; Include Device Definition File	call tim	
(ST72C254G2.str)		bset pbdr,≉2	
		ret	
segment	: 'Rom' ; Code segment - Address defined in		
Definition file	(.str)	light_2: nop	
		bres pbdr,#3	
Start:	; Reset Label	call tim	
		bset pbdr,#3	
	:**********Timer A Setup Code	ret	
	1d a.#\$40		
	ld TACR1, a	light_3: ncp	
	:*******Timer à setup code	bres pbdr,#4	
	1d a.#\$80	call tim	
	ld TACR2 a	bset pbdr,#4	
	:*Timer A Output Compare Register 1	ret	
	1d a.#580		
	ld TACCIHR, a	light_4: nop	
	1d a.#\$00	bres pbdr,#5	
	1d TADCILR.a	call Lim	
		bset pbdr.#5	
	: ********* Port & Setup Code	ret	
	ld a, #500 ; Bit On = Output, Bit Off		
= Input		light_5: nop	
	Id PADDR, a ; Port A Direction	bres pbdr,#6	
Register		call tim	
	Id a. #\$FF : Cotion Selects Input or	bset pbdr.#6	
Output Type	· · · · · · · · · · · · · · · · · ·	ret	
opphare ()pc	1d PAOR, a ; Port A Option Register		
	i i i i i i i i i i i i i i i i i i i	light 6: nop	
	: ******** Port B Setup Code	bres pbdr, #7	
	ld a. #SFF ; Bit On = Output, Bit Off	call tim	
a loout		bset pbdr,#7	
- Thpue	1d PRDOR, a Port B Direction	ret	
Panister	to result a strend b concerned		
Regrater	1d a. #\$00 : 0CMP1A.	tim: nop	
	Id PROR. a : Port B Option Register	call timer	
	Tu though a strate b operation negrated	1d a.*\$3F	
	. strateset Control Registers	ld stav.a	
	Id a #\$DQ	nop	
	Id HISCRI a Slow Mode : Fonu =	ret	
40000004	ig albert, a , stor about the		
400000012	· Fill Interrupt on Falling and Rising Edge	: ******** End Tiver A Interrupt	
	, cto incertape on tarring and money cage	segrent 'vectit' ; Vector segme	nt -
	Id a #500 : Clock Filter Interrupt	Address defined in Definition file (.str)	
Disabled			
Disabied	Id CRSR, a	DC.W O	
		DC.W 0	
	Id a. #\$7F : Stack Pointer Low Byte	DC.W 0	
	ld s. a ; Stack Pointer High Byte set by	DC.W 0	
hardware		DC.W 0	
	ld a. #S7F : Watchdog Control	DC.W 0	
Register		DC.W O	
	1d WDGCR, a : Watchdog Disabled	DC.W 0	
		DC.N 0	
	rim : Global Interrupt Enable	TimA: DC.W TimerA	
	,	0 V.3G	
Vector File fod	P	DC.W D	
: ST7 Vector Fi	le - Created by Application Builder	O W.CU	
		EO: DC.W O	
	Interrupt Service Routines	DC.W D	
; File	Created with Device Set to ST72C254G2	reset: DC.W Start	
seamen	t 'Rom' ; Code segment - Address defined in		
Definition file	(.str)	end	
TimerA: : ****	***** TimerA Interrupt ISR Code Here		
	*************************	Main Source Code	
CHANGI	ING THE SEQUENCE BELOW WILL *	ST7 Main File - Created by Application Builder	
ALTER	THE SEQUENCE OF THE LIGHTS *	; Author : Tony Ashton	
IN CR	TO UPDATE AT THE CLUB	: Company : Kanda Systems Ltd	
. ************	********************************	: Comment : Code for Disco Lights/Easy to update in	club
TAOF :	; Test TOF Flag (TASR.5)	environment	
nop			
call	seq 1	; File Created with Device Set to ST72C254G2	
ira T/	NOF	.include \$SetupFileName ; Set up file (.sts) is in	ncluded
nop		here	
iret		segment 'RamO' ; RAM Page Zero segment - Always at A	DDRESS
-		\$80-\$FF	
seq 1: call 1	fight_1	;Define user variables here if required - Example Syn	cax is
call 1	light_2	;COUNTER DS.B 1	
call 1	light 3	;COUNTER2 DS.W 1	
call	light 4	PIN NUMBER EQU 6	
call 1	light 5	A REAL PROPERTY AND A REAL	
call	light 6	comp dc.b \$00	
call	light 5	stay cc.b \$3F	
call	light 4	segrent 'Rom' ; Code segrent - Address defi	ined in
call	light 3	device file (.str)	
call	light 2	set: nop ;default setup co	ode
ret		1d a,*\$26	
**********	**********	ld TAOC1HR, a	

	14	
	10 a,#366	
	clr TAACLR	timer & alternate counter
ow register	err moun	The second control control
	jra matn	
ain:	ld a.#\$FF;stays h	ere until interrupt on port
is generated		
	ld pbdr.a	
	nop	
	nop	to main until intermunt in
etected	Jia warm (returns	so nam until interrupt 15
etectes		
imer:	nop	
	btjf padr.#0,defa	ult
	<pre>btjf padr,#1,time</pre>	r_25
	<pre>btjf padr,#2,time</pre>	r_50 ;sets speed of
ights from 25-75	percent	
	btjf padr,#3,time	r_/5
	Jra GI	
1:	clr TAACLR	sets TACR to
FFD		
	пор	
	nop	
	ld a,TAOCILR	;loads value into
comulator		
2:	cp a,TAACLR	
	Jrule high	
	ira d2	
itah:	ld a. TAOCIHR	
igh 1: cp a, TAA	CHR	
	jrule go ;jumps t	o go if TAACHR lessthan or
equals to zero		
	nop	
	jra high_1	;returns to compare if
AACHR is greater	than zero	
io:	nop	
	co a. 4500	
	irule on 1	ends if stay is lessthan
or equal to zero	5.0.0 90_1	Terres to stay is respondit
	dec stay ;decreme	ents value in order to reach
and of sequence	,	
	jra dl	:jumps to d1 if stay is
greaterthan zero		
Jo_1:	ret	jumps out of sequence and
turns on another	light	**
	TIMING INTEDWALC	
*******	***************	••
	e Alatan	interval for 15 percent
default: ld a,#\$2	in trung	incerval for 15 percent
default: ld a,#\$2 t3:	btjf padr,#0,t3	;debounce
default: ld a,#\$2 t3:	btjf padr,#0,t3 ld TAOC1HR,a	;debounce
default: ld a,#\$2 t3:	btjf padr,#0,t3 ld TAOC1HR,a ld a,#\$66	;debounce
default: ld a,#\$2 t3:	<pre>btjf padr,#0,t3 ld TAOC1HR,a lc a,#\$66 ld TAOC1LR,a</pre>	;debounce
default: ld a,#\$2 13:	<pre>btjf padr,#0,t3 btjf padr,#0,t3 ld TAOC1HR,a ld a,#\$66 ld TAOC1LR,a jra dl</pre>	;debounce ;returns to dl
default: 1d a,#\$2 t3:	<pre>btjf padr,#0,t3 btjf padr,#0,t3 ld TAOCIHR,a ld a,#\$66 ld TAOCILR,a jra dl </pre>	;returns to dl
default: 1d a,#\$2 t3: timer_25:1d a,#\$3 t0:	<pre>btjf padr.#1.013 ld TAOCIHR.a ld TAOCILR.a jra dl iF ;timing btif padr.#1.00</pre>	;returns to d1 interval for 25 percent :debounce
default: 1d a,#\$2 t3: timer_25:1d a,#\$3 t0:	<pre>btjf padr.#1,t0 btjf value ld TAOCLHR.a ld a.#\$66 ld TAOCLLR.a jra dl iF ;timing btjf padr.#1,t0 ld TAOCLHR.a</pre>	;returns to dl interval for 25 percent ;debounce
default: 1d a,#\$2 t3: tim <mark>er_25:1d</mark> a,#\$3 t0:	 timing tifpadr, Ø.t.3 Id TAOCIHR, a Id a, 4\$66 Id TAOCILR, a jra dl if ; timing btjf padr, #1, tO Id TAOCIHR, a Id a, 4\$FF 	;returns to dl interval for 25 percent ;debounce
default: 1d a,#\$2 13: tim <mark>er_</mark> 25:1d a,#\$3 t0:	<pre>b ; timing btjf padr. (Ø.t3 ld TAOCLIR.a ld TAOCLIR.a jra dl btjf padr.el.to ld TAOCLIR.a ld a.sfF ld TAOCLIR.a</pre>	;returns to dl interval for 25 percent ;debounce
default: 1d a,#\$2 13: tim <mark>er_25:1d</mark> a,#\$3 t0:	<pre>b ; timing b tjr padr. (Ø.t3 ld TAOCIHR.a ld a. 4566 ld TAOCILR.a jra dl btjf padr. #1.t0 ld TAOCIHR.a ld a. 45FF ld TAOCILR.a jra dl</pre>	;returns to dl ;returns to dl interval for 25 percent ;debounce ;returns to dl
default: ld a,#\$2 :3: tim <mark>er_25:ld a,#\$3</mark> :0:	<pre>b ;tming btjfpadr,0,t3 ld TAOCIHR,a ld TAOCIHR,a jra dl F ;timing btjf padr,01,t0 ld TAOCIHR,a ld a,*\$FF ld TAOCILR,a jra dl</pre>	returns to d1 ;returns to d1 interval for 25 percent ;debounce ;returns to d1
default: ld a,#\$2 t3: timer_25:ld a,#\$3 t0: timer_50:ld a,#\$7	<pre>b ; timing btjf padr. (0.t3 ld TAOCIHR.a ld a. \$566 ld TAOCILR.a jra dl F ; timing btjf padr. #1.t0 ld TAOCIHR.a ld a. \$5FF ld TAOCIHR.a jra dl F ; timing btjf vadr. 41 ef ; timing btjf vadr. 41 ef ; timing</pre>	;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent
default: ld a,#\$2 t3: timer_25:ld a,#\$3 t0: timer_50:ld a,#\$7 t1:	<pre>b j;tming bijf padr, 40,t3 ld TAOCLIR,a ld TAOCLIR,a jra dl if itiming btjf padr,41,t0 ld TAOCLR,a jra dl if ;timing btjf padr,42,t1 ld TAOCLR,a jra dl</pre>	returns to dl ;returns to dl interval for 25 percent ;debounce ;returns to dl interval for 50 percent
default: 1d a,#\$2 23: timer_25:1d a,#\$3 t0: timer_50:1d a,#\$7 t1:	<pre>b ; timing btjf padr. #0.t3 ld TAOCIHR.a ld a. #\$66 ld TAOCILR.a jra d1 k ; timing btjf padr.#1.t0 ld TAOCIHR.a ld a. #\$FF ld TAOCILR.a jra d1 %F ; timing btjf padr.#2.t1 ld TAOCIHR.a ld a. #\$FF</pre>	returns to dl ;returns to dl interval for 25 percent ;debounce ;returns to dl interval for 50 percent
default: ld a,#\$2 :3: timer_25:ld a,#\$3 :0: timer_50:ld a,#\$7	<pre>b ; timing btjf padr. #0.t3 ld TAOCIHR.a ld TAOCIHR.a jra dl btjf padr.#1.t0 ld TAOCIHR.a ld a.\$FF ld TAOCIHR.a ld a.\$FF stiming btjf padr.#2.t1 ld TAOCIHR.a ld a.\$FF ld TAOCIHR.a</pre>	returns to dl interval for 25 percent ;debounce ;returns to dl interval for 50 percent
default: ld a,#\$2 13: timer_25:ld a,#\$3 10: timer_50:ld a,#\$7 tl:	<pre>b ; timing btjf padr, #0.t3 ld TAOCIHR.a ld TAOCIHR.a jra dl btjf padr, #1.t0 ld TAOCIHR.a ld a.*SFF ld TAOCILR.a jra dl Pf ; timing btjf padr, #2.t1 ld TAOCIHR.a ld a.\$SFF ld TAOCIHR.a ld a.\$SFF ld TAOCIHR.a ld a.\$SFF</pre>	returns to dl ;returns to dl interval for 25 percent ;debounce ;returns to dl interval for 50 percent
default: ld a,#\$2 13: timer_25:ld a,#\$3 10: timer_50:ld a,#\$7 tl:	<pre>b ;timing btjf padr,#0,t3 ld TAOCLIR,a ld TAOCLIR,a jra dl # ;timing btjf padr,#1,t0 ld TAOCLIR,a ld A,#\$FF ld TAOCLIR,a ld a,#\$FF ld TAOCLIR,a jra dl</pre>	returns to dl ;returns to dl interval for 25 percent ;debounce ;returns to dl interval for 50 percent
default: 1d a,#\$2 23: timer_25:1d a,#\$3 t0: timer_50:1d a,#\$7 t1: timer_75:1d a,#\$5	<pre>b ; timing btjf padr, #0,t3 ld TAOCIHR,a ld a, 4\$66 ld TAOCILR.a jra dl F ; timing btjf padr,#1,t0 ld TAOCIHR,a ld a, 4\$FF ld TAOCILR,a jra dl PF ; timing btjf padr,#2,t1 ld TAOCILR,a jra dl 3FF; timing 3F</pre>	returns to d1 ;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent
default: ld a,#\$2 13: timer_25:ld a,#\$3 to: timer_50:ld a,#\$7 tl: timer_75:ld a,#\$6 t2:	<pre>b ; timing btjf padr.#0.t3 ld TAOCIHR.a ld a.4566 ld TAOCILR.a jra dl F :timing btjf padr.#1.t0 ld TAOCIHR.a ld a.45FF ld TAOCILR.a jra dl F :timing btjf padr.#2.t1 ld TAOCIHR.a ld a.45FF ld TAOCIHR.a ld a.45FF ld TAOCIHR.a ld a.45FF ld TAOCIHR.a jra dl 3F :timing btjf padr.43.t2</pre>	interval for 75 percent ;debounce ;returns to d1 interval for 50 percent interval for 50 percent
<pre>tefault: ld a,#\$2 c3: timer_25: ld a,#\$3 to: timer_50: ld a,#\$7 t1: timer_75: ld a,#\$5 t2:</pre>	<pre>b ; timing btjf padr. #0.t3 ld TAOCIHR.a ld a. #\$66 ld TAOCILR.a jra dl btjf padr.#1.t0 ld TAOCIHR.a ld a. #\$FF ld TAOCIHR.a ld a. #\$FF id TAOCIHR.a ld a. #\$FF ld TAOCIHR.a jra dl sF ; timing btjf padr.#2.t1 ld TAOCIHR.a jra dl sF ; timing btjf padr.#2.t1 ld TAOCIHR.a jra dl sF ; timing btjf padr.#3.t2</pre>	<pre>interval for 75 percent ;debounce ;returns to dl interval for 25 percent ;debounce ;returns to dl interval for 50 percent interval for 75 percent</pre>
default: ld a,#\$2 13: timer_25:ld a,#\$3 10: timer_50:ld a,#\$7 tl: timer_75:ld a,#\$5 t2:	<pre>b ; timing btjf padr, 40,t3 ld TAOCIHR,a ld a, 4566 ld TAOCILR,a jra dl F ; timing btjf padr,41,t0 ld TAOCIHR,a ld a, 45FF ld TAOCILR,a jra dl btjf padr,42,t1 ld TAOCIHR,a ld a, 45FF ld TAOCIHR,a jra dl Bf ; timing btjf padr,43,t2 ld TAOCIHR,a ld a, 45FF ld TAOCIHR,a ld TAOCIHR,a ld TAOCIHR,a</pre>	interval for 75 percent ;debounce ;returns to dl interval for 50 percent interval for 75 percent
default: ld a,#\$2 t3: timer_25:ld a,#\$3 t0: timer_50:ld a,#\$7 t1: timer_75:ld a,#\$5 t2:	<pre>b ; timing btjf padr, #0,t3 ld TAOCIHR,a ld a, 4\$66 ld TAOCIHR,a jra dl ff ; timing btjf padr,#1,t0 ld TAOCIHR,a ld a, 4\$FF ld TAOCIHR,a ld a, 4\$FF ld TAOCIHR,a jra dl #F ; timing btjf padr,#2,t1 ld TAOCIHR,a ld a, 4\$FF ld TAOCIHR,a ld a, 4\$FF ld TAOCIHR,a ld a, 4\$FF ld TAOCIHR,a ld a, 4\$FF</pre>	interval for 25 percent ;debounce ;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent
timer_25:1d a,#\$2 timer_25:1d a,#\$3 to: timer_50:1d a,#\$7 t1: timer_75:1d a,#\$8	<pre>b j;tming btjf padr, 40,t3 ld TAOCIHR,a ld a, 4566 ld TAOCILR,a jra dl F :timing btjf padr,#1,t0 ld TAOCIHR,a ld a, 45FF ld TAOCIHR,a ld a, 45FF ld TAOCIHR,a ld a, 45FF ld TAOCIHR,a ld A, 5FF ld TAOCIHR,a jra dl</pre>	interval for 25 percent ;debounce ;returns to d1 interval for 25 percent ;returns to d1 interval for 50 percent interval for 75 percent
<pre>tefault: ld a,#\$2 (3: timer_25: ld a,#\$3 to: timer_50: ld a,#\$7 t1: timer_75: ld a,#\$5 t2: : Inter:</pre>	<pre>b ; timing btjf padr. #0.t3 ld TAOCIHR.a ld a. #\$66 ld TAOCILR.a jra dl btjf padr.#1.t0 ld TAOCIHR.a ld a. #\$FF ld TAOCIHR.a ld a. #\$FF id TAOCIHR.a ld a. #\$FF id TAOCIHR.a jra dl sF ; timing btjf padr.#2.t1 ld TAOCIHR.a ld a. #\$FF id TAOCIHR.a jra dl sF ; timing btjf padr.#3.t2 ld TAOCIHR.a jra dl sF ; timing btjf padr.#3.t2 ld TAOCIHR.a ld a. #\$FF ld TAOCIHR.a ld a. #\$FF</pre>	interval for 25 percent ;debounce ;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent interval for 75 percent
<pre>tefault: ld a,#\$2 i3: timer_25: ld a,#\$3 i0: timer_50: ld a,#\$7 tl: timer_75: ld a,#\$5 t2: ; Inter: ; This i</pre>	<pre>b ; timing btjf padr, #0,t3 ld TAOCIHR,a ld a,4\$66 ld TAOCILR,a jra dl # ; timing btjf padr,#1,t0 ld TAOCIHR,a ld a,4\$FF ld TAOCIHR,a jra dl # ; timing btjf padr,#2,t1 ld TAOCIHR,a jra dl # ; timing btjf padr,#2,t1 ld TAOCIHR,a jra dl # ; timing btjf padr,#3,t2 ld TAOCIHR,a ld a,4\$FF ld TAOCIHR,a ld a,4\$FF</pre>	<pre>interval for 75 percent ;debounce ;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent interval for 75 percent STV File called here 1 Intervupt Service Routines</pre>
<pre>tefault: ld a,#\$2 timer_25: ld a,#\$3 to: timer_50: ld a,#\$7 tl: timer_75: ld a,#\$5 t2: ; Inter: ; This t (ISR)</pre>	 timing timing tif padr. #0.t3 tAOCLIR.a jra dl tif tAOCLIR.a jra dl timing btjf padr. #1.t0 tAOCLIR.a jra dl timing btjf padr. #2.t1 tAOCLIR.a jra dl timing btjf padr. #3.t2 tAOCLIR.a jra dl timing tjr padr. #3.t2 tAOCLIR.a jra dl rupts in use, so . file contains shell 	<pre>interval for 75 percent ;debounce ;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent interval for 75 percent STV File called here 1 Interrupt Service Routines</pre>
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<pre>tefault: ld a,#\$2 c3: timer_25: ld a,#\$3 to: timer_50: ld a,#\$7 tl: timer_75: ld a,#\$5 timer_75: ld a,#\$5 timer_15: timer_15: timer_25: ld a,#\$5 timer_15: ld</pre>	<pre>b ; timing btjf padr.#0.t3 ld TAOCIHR.a ld a.4566 ld TAOCILR.a jra dl F :timing btjf padr.#1.t0 ld TAOCIHR.a ld a.45FF ld TAOCILR.a jra dl F :timing btjf padr.#2.t1 ld TAOCIHR.a ld a.45FF ld TAOCIHR.a ld a.45FF ld TAOCIHR.a ld a.45FF ld TAOCIHR.a ld a.45FF ld TAOCIHR.a jra dl SF :timing btjf padr.#3.t2 ld TAOCIHR.a ld a.45FF ld a.45FF ld TAOCIHR.a ld a.45FF ld a.45FFF ld a.45FF ld a.4</pre>	<pre>interval for 75 percent ;debounce ;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent interval for 75 percent STV File called here 1 Interrupt Service Routines ble. Add Your ISR Code to</pre>
<pre>tefault: ld a,#\$2 (3: timer_25: ld a,#\$3 to: timer_50: ld a,#\$7 tl: timer_75: ld a,#\$7 (1SR) ; and In this file</pre>	<pre>b ; timing btjf padr. #0.t3 ld TAOCIHR.a ld a. #\$66 ld TAOCILR.a jra dl btjf padr.#1.t0 ld TAOCIHR.a ld a. #\$FF ld TAOCIHR.a ld a. #\$FF itiming btjf padr.#2.t1 ld TAOCIHR.a ld a. #\$FF itiming btjf padr.#2.t1 ld TAOCIHR.a ld a. #\$FF itiming btjf padr.#3.t2 ld TAOCIHR.a jra dl sF ; timing btjf padr.#3.t2</pre>	<pre>interval for 75 percent ;debounce ;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent interval for 75 percent STV File called here I Interrupt Service Routines ble. Add Your ISR Code to</pre>
<pre>tefault: ld a,#\$2 i3: timer_25: ld a,#\$3 i0: timer_50: ld a,#\$7 tl: timer_75: ld a,#\$5 t2:</pre>	 jtiming <	<pre>interval for 75 percent ;debounce ;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent interval for 75 percent STV File called here 1 Interrupt Service Routines ble. Add Your ISR Code to ; Interrupt and Vector</pre>
<pre>tefault: ld a,#\$2 (3: timer_25: ld a,#\$3 to: timer_50: ld a,#\$7 timer_75: ld a,#\$7 tit: timer_75:</pre>	<pre>b ; timing btjf padr, #0,t3 ld TAOCIHR,a ld a, #\$66 ld TAOCIHR,a jra dl F ; timing btjf padr,#1,t0 ld TAOCIHR,a ld a, #\$FF ld TAOCIHR,a ld a, #\$FF ld TAOCIHR,a ld a, #\$FF ld TAOCIHR,a jra dl Bf ; timing btjf padr,#2,t1 ld TAOCIHR,a jra dl Bf ; timing btjf padr,#3,t2 ld TAOCIHR,a jra dl sf ; timing btjf padr,#3,t2 ld TAOCIHR,a jra dl sf ; timing btjf padr,#3,t2 ld TAOCIHR,a jra dl rupts in use, so . file contains shell nterrupt Vector Ta de SVectorFileName uded here end</pre>	<pre>interval for 25 percent ;debounce ;returns to d1 interval for 25 percent ;debounce ;returns to d1 interval for 50 percent interval for 75 percent STV File called here I Interrupt Service Routines ble. Add Your ISR Code to ; Interrupt and Vector</pre>
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PARTS LIST

Kanda ST7 Starter Kit or Equivalent Circuit Using ST72254G2 Device ST72254G2 Integrated Circuit 6 x 6 pin dual-in-line sockets

6 x MOC3020 Optocoupler, Triac Drivers 6 x Z0410DF 4 Amp 400 Volt Triacs

1 x 9-way 'Male' 'D' Connector

- 1 x 9-way 'Female' 'D' Connector
- 1 X 9-Way Terriale D Connecto
- 6 x 330R Resistors

- 6 x 560R Resistors
- 7 x 2-way terminal blocks
- 1 x Fuse Holder
- 1 x 2.6 Amp Fuse
- 6 x 100 Watt 240 volts Disco Lights
- 1 x Plastic Hard Case to Cover PCB.

Conclusion

This project is based around the S172254G2 flash device, which can be used with the Kanda S17 Starter Kit, or alternatively a circuit can be built dedicated to this project based around the SI72254G2 device. A circuit showing a simple design for the flash device is given in Appendix 2. The project uses the flash device to control the operation of disco lights, which operate at 240 volts ac. The only other circuit that was used within the project was the power controller board, which was used to supply the necessary voltage to the disco lights. This circuit was based around a triac, and triac driver that provide complete isolation from the Kanda ST7 Starter Kit. The circuit will operate up to six disco lights moving in what ever sequence the user requires. Using the flash device gives an added advantage of updating the software within the club environment using one of Kanda's keyfobs. This will allow the code to be changed to develop a new sequence for the disco lights, and update them simply by plugging the keyfob into the board and inserting the code into the flash device. When the full circuit was built the software worked well on the Kanda ST7 Starter Kit and with the Flash circuit shown in Appendix 2.

S17 Starter Kits are available from Kanda Systems Ltd. They are available as a 32pin version - Order code ST7KND1-Kit 2 Price \$160 or 42pin or 56pin versions - Order code ST7KND2 - Kit 2 Price \$160

Phone Kanda Sales Hotline +44 1970 621030 or Fax +44 1970 621040 e-mail sales@kanda.com or view the website www.kanda.com

Other auxillary parts to create the disco lights project are available from component suppliers like Maplin Electronics

Appendix 1

The code shown on the next page are the three files generated from the ST assembler, as mentioned earlier in this article. They include the set-up file, vector file, and the main program file:

Appendix 2

The schematic diagram shown in Figure 4 will enable the user to run the ST72254 flash device directly from the circuit that will connect to the power controller board via the 9-way 'D' type socket. The circuit will also allow the user to update the software to the device by simply inserting a keyfob. The keyfob is a device available for in-situ programming within the club environment, so eliminating the use of a PC for programming.

PERFECT MIRROR Used as a Light Guide

by Reg Miles

uilding on the 'perfect mirror' they created in 1998, Massachusetts Institute of Technology researchers have proposed a new kind of coaxial cable that may be able to carry light over long distances and around sharp bends while retaining its polarisation.

The 'perfect mirror' is so-dubbed because it combines the best of both metallic and dielectric mirrors. A conventional metallic mirror reflects light omnidirectionally; it also absorbs a significant portion of the incident light. A dielectric mirror is composed of multiple layers of transparent dielectric materials, which can be made to be extremely low loss compared to metal; and it reflects a prescribed range of frequencies coming from within a limited set of angles. The 'perfect mirror' reflects light from all angles and polarisations like a metallic mirror, but can be as low loss as a dielectric mirror, thanks to the properties of its multi-laver coating.

The researchers were Professors John D. Joannopoulos and Edwin L. Thomas, with Shanhui Fan and Yoel Fink, whose idea it was. Then, joined by Mihai Ibanescu, they put Fink's subsequent idea into practice and made a tube out of the 'perfect mirror' to create an omnidirectional waveguide. This has the significant advantage that it can bend the light in a shorter distance than an optical fibre; making it ideal for use in devices such as optical chips. It can also accomodate a greater bandwidth of light, with lower attenuation and less dispersion thus getting more information to travel over longer distances.

Joannopoulos then proposed a coaxial version of it, with the inner and outer sheaths coated with the dielectric layers and the light propagated in the air-space between them. The researchers have launched a new company in Cambridge, Mass., called OmniGuide Communications, to explore the practicalities and possibilities of this and the multi-layer coatings.

According to the company: "The OmniGuide Fibre could substantially reduce or even eliminate the need for amplifiers in optical networks. Secondly it will offer a bandwidth capacity that could potentially be several orders of magnitude greater than conventional single-mode optical fibres. Elimination of the need for optical amplifiers will cut the cost of deploying and maintaining optical networks. Combined with the increased bandwidth, it will allow network operators to slash the cost-per-bit dramatically."

And according to Joannopoulos: 'What's important about this is that it has opened a new direction for experimental research that was not possible before. It's important to push along in this direction and see if we can find materials and fabrication approaches that will make this happen. We do know if we can do what the theory says, it will happen. This may be a breakthrough in bridging the very different requirements for transmitting infrared and radio frequencies at opposite ends of the energy spectrum. And the nice thing about it is that whatever you put in, you get out. This could make a big difference where polarisation is an issue."

For additional information contact: info@omni-guide.com